

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE (USPTO)**

<b>Serial Number</b>	09/927,426
<b>Confirmation Number</b>	6413
<b>Filing Date</b>	August 10, 2001
<b>Title of Application</b>	Delay Circuit and Method
<b>First Named Inventor</b>	William A. Phillips
<b>Assignee</b>	ST Microelectronics, Inc. (large entity)
<b>Group Art Unit</b>	2816
<b>Examiner</b>	Kenneth Wells
<b>Attorney Docket Number</b>	1768-041
<b>Nature of the Office Communication to which this is responding</b>	Non-Final Office Action
<b>Date of the Office Communication</b>	June 7, 2010
<b>Nature of this Document</b>	Response to Non-Final Office Action

**CERTIFICATE OF MAILING OR TRANSMISSION**

I hereby certify that this correspondence is being transmitted via the Office electronic filing system, EFS-Web, addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 7<sup>th</sup> day of October, 2010.

/Cindy McKee/

Signature